

 Pending Active

- L1: (232) temporal adj5 logic
- L2: (30639) state adj3 machine
- L3: (15813) condition\$3 near9 transition
- L4: (22) 11 and 12 and 13
- L5: (20) 14 and cod\$3
- L6: (8) 15 and emulat\$4
- L7: (1) 20030046658.pn.

 Failed Saved Favorites Tagged (0) UDC

DBs: USPAT DSSPGPLB      Plots:  Highlight hit terms initially

Default operator: OR

L5 and emulat\$4

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1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20040025122	Hardware-based HDL code coverage and design analysis	716,
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030182642	Hardware debugging in a hardware description language	716,
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 20030131325	Method and user interface for debugging an electronic system	716,
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